



Vincotech

10-EY126PB009MS-PJ19F78T

datasheet

flowPACK E2 SiC

1200 V / 9 mΩ

Topology features

- 3ph Inverter
- Low and high side Kelvin Emitter for improved switching performance
- MOSFET
- Open Emitter configuration
- Temperature sensor

Component features

- High Blocking Voltage with low drain source on state resistance
- High speed SiC-MOSFET technology
- Resistant to Latch-up

Housing features

- Base isolation: Al₂O₃
- Convex shaped substrate for superior thermal contact
- Compact housing
- CTI600 housing material
- Thermo-mechanical push-and-pull force relief
- Press-fit pin
- Reliable cold welding connection

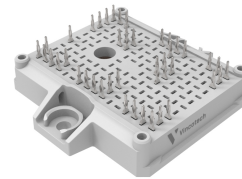
Target applications

- Elevator Drives
- Embedded Drives
- Servo Drives

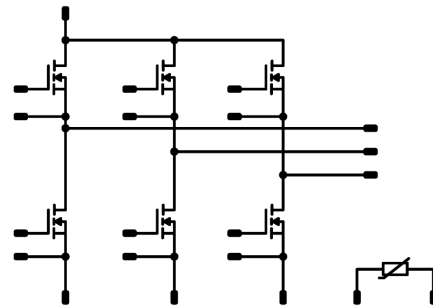
Types

- 10-EY126PB009MS-PJ19F78T

flow E2 12 mm housing



Schematic





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Maximum Ratings

$T_j = 25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Inverter Switch				
Drain-source voltage	V_{DS}		1200	V
Drain current (DC current)	I_D	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	112	A
Peak drain current	I_{DM}	t_p limited by T_{jmax}	568	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	161	W
Gate-source voltage	V_{GS}	static	-5 / 18	V
		dynamic	-10 / 22	V
Maximum Junction Temperature	T_{jmax}		175	°C

Module Properties

Thermal Properties

Storage temperature	T_{stg}		-40...+125	°C
Operation temperature under switching condition	T_{jop}		-40...+($T_{jmax} - 25$)	°C

Isolation Properties

Isolation voltage	V_{isol}	DC Test Voltage* $t_p = 2\text{ s}$	6000	V
Creepage distance			>12,7	mm
Clearance			9,11	mm
Comparative Tracking Index	CTI		≥ 600	

*100 % tested in production



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Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	

Inverter Switch

Static

Drain-source on-state resistance ⁽¹⁾	$r_{DS(on)}$		18		142	25 175		8,5 13,5	12,5	mΩ
Gate-source threshold voltage	$V_{GS(th)}$				0,0142	25	1,7	2,25	2,75	V
Gate to Source Leakage Current	I_{GSS}		22	0		25			200	nA
Zero Gate Voltage Drain Current	I_{DSS}		0	1200		25			20	μA
Internal gate resistance	r_g							0,5		Ω
Gate charge	Q_g		-5/18	800	142	25		376		nC
Short-circuit input capacitance	C_{iss}	$f = 500$ kHz	0	800	0	25		9360		pF
Short-circuit output capacitance	C_{oss}							470		
Reverse transfer capacitance	C_{rss}							16		
Diode forward voltage	V_{SD}		0		142	25		4,1		V

Thermal

Thermal resistance junction to sink ⁽²⁾	$R_{th(j-s)}$	$\lambda_{paste} = 5,2$ W/mK (PTM)						0,59		K/W
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datasheet

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit	
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max		
Dynamic											
Turn-on delay time	$t_{d(on)}$	$R_{gon} = 2 \Omega$ $R_{goff} = 2 \Omega$	-5/15	600	140	25		32,7		ns	
						125		30,02			
						150		29,63			
Rise time	t_r					25		18,05			ns
						125		13,58			
						150		13,11			
Turn-off delay time	$t_{d(off)}$					25		39,5			ns
						125		44,32			
						150		45,65			
Fall time	t_f					25		10,16			ns
						125		10,56			
						150		10,78			
Turn-on energy (per pulse)	E_{on}	$Q_{rFWD}=0,764 \mu C$ $Q_{rFWD}=2,12 \mu C$ $Q_{rFWD}=2,5 \mu C$	25		3,69			mWs			
			125		3,8						
			150		3,92						
Turn-off energy (per pulse)	E_{off}		25		0,503			mWs			
			125		0,541						
			150		0,557						
Peak recovery current	I_{RRM}	$di/dt=3021 A/\mu s$ $di/dt=7048 A/\mu s$ $di/dt=8966 A/\mu s$	25		29,72			A			
			125		63,1						
			150		76,24						
Reverse recovery time	t_{rr}		25		49,72			ns			
			125		48,09						
			150		48,99						
Recovered charge	Q_r		25		0,764			μC			
			125		2,12						
			150		2,5						
Reverse recovered energy	E_{rec}		25		0,147			mWs			
			125		0,515						
			150		0,633						
Peak rate of fall of recovery current	$(di_{rr}/dt)_{max}$	25		1835,74			A/ μs				
		125		8530,87							
		150		10539,44							



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Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	

Thermistor

Static

Rated resistance	R					25		5		kΩ
Deviation of R100	$\Delta_{R/R}$	$R_{100} = 499 \Omega$				100	3,2		3,3	%
Power dissipation	P					25		130		mW
Power dissipation constant	d					25		1,3		mW/K
B-value	$B_{(25/50)}$	Tol. $\pm 1 \%$						3380		K
Vincotech Thermistor Reference									V	

⁽¹⁾ Value at chip level

⁽²⁾ Only valid with pre-applied Vincotech thermal interface material.



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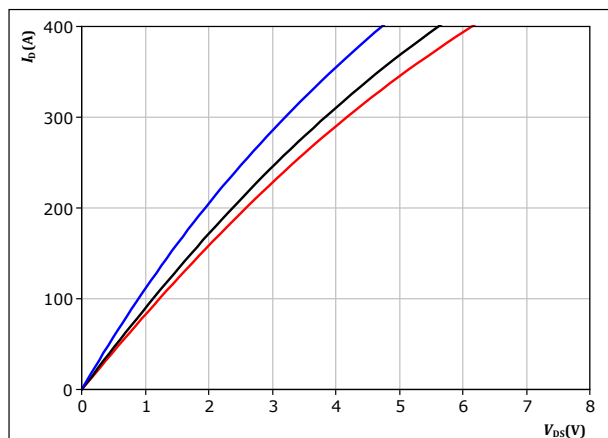
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Inverter Switch Characteristics

figure 1. MOSFET

Typical output characteristics including $R_{DS(on)} + R_{DS(off)}$

$$I_D = f(V_{DS})$$

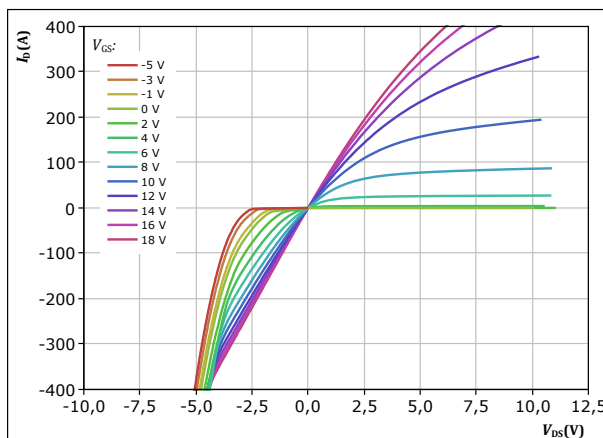


$t_p = 250 \mu s$
 $V_{GS} = 18 V$
 $T_j: 25^\circ C$
 $125^\circ C$
 $150^\circ C$

figure 2. MOSFET

Typical output characteristics including $R_{DS(on)} + R_{DS(off)}$

$$I_D = f(V_{DS})$$

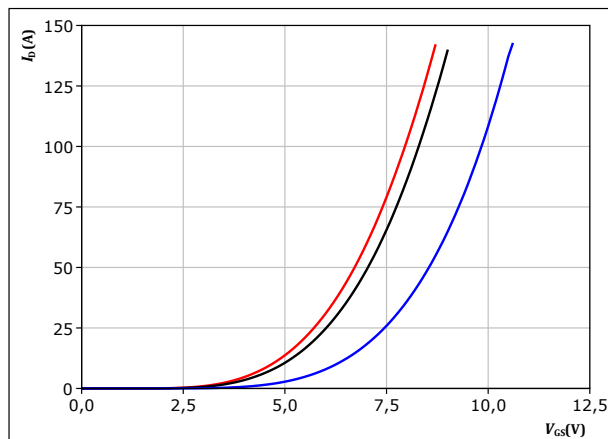


$t_p = 250 \mu s$
 $T_j = 150^\circ C$
 V_{GS} from -5 V to 18 V in steps of 2 V

figure 3. MOSFET

Typical transfer characteristics

$$I_D = f(V_{GS})$$

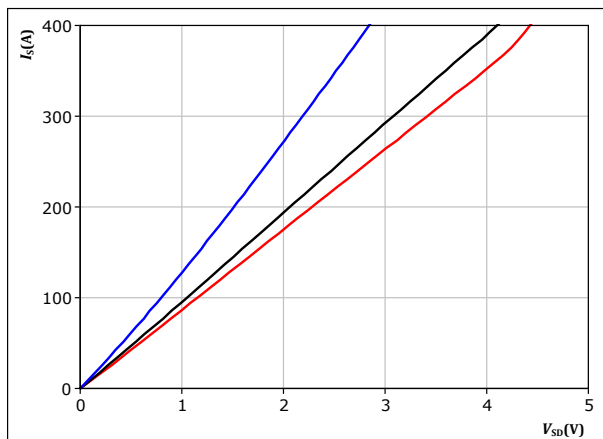


$t_p = 250 \mu s$
 $V_{DS} = 23 V$
 $T_j: 25^\circ C$
 $125^\circ C$
 $150^\circ C$

figure 4. MOSFET

Typical reverse drain current characteristics including $R_{DS(on)} + R_{DS(off)}$

$$I_{SD} = f(V_{SD})$$



$t_p = 250 \mu s$
 $V_{GS} = 18 V$
 $T_j: 25^\circ C$
 $125^\circ C$
 $150^\circ C$



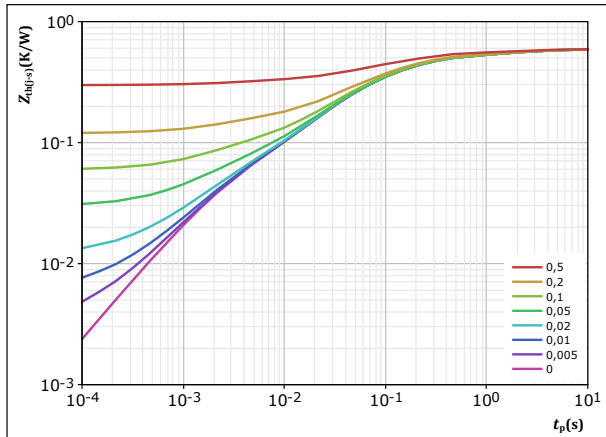
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Inverter Switch Characteristics

figure 5. MOSFET

Transient thermal impedance as a function of pulse width

$$Z_{th(j-a)} = f(t_p)$$



$$D = \frac{t_p}{T}$$

$$R_{th(j-a)} = 0,591 \text{ K/W}$$

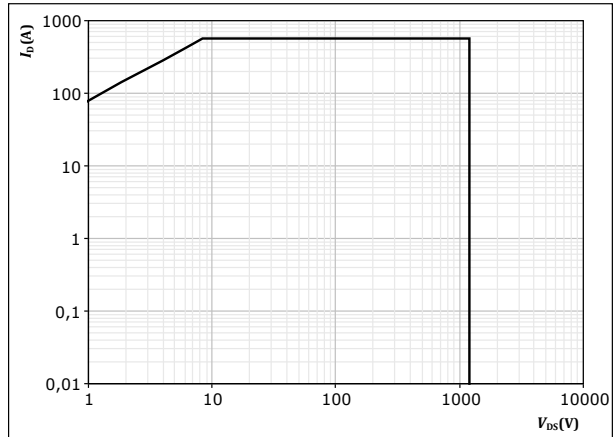
MOSFET thermal model values

R (K/W)	τ (s)
2,63E-02	7,75E+00
8,39E-02	1,40E+00
2,65E-01	1,46E-01
1,86E-01	3,39E-02
3,66E-02	2,17E-03

figure 6. MOSFET

Safe operating area

$$I_D = f(V_{DS})$$



$D = \text{single pulse}$

$$T_s = 80 \text{ } ^\circ\text{C}$$

$$V_{GS} = 18 \text{ V}$$

$$T_j = T_{jmax}$$



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10-EY126PB009MS-PJ19F78T
datasheet

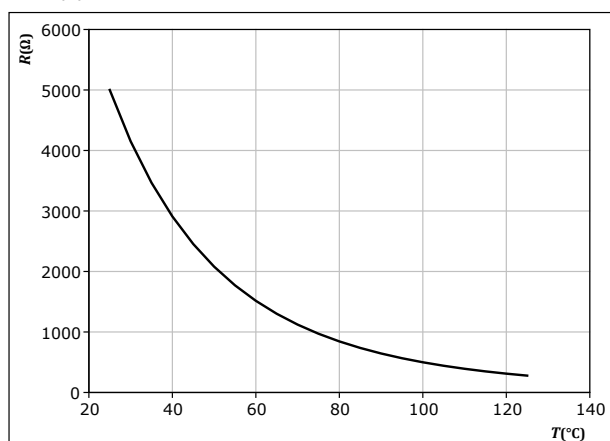
Thermistor Characteristics

figure 7.

Thermistor

Typical NTC characteristic as function of temperature

$$R_T = f(T)$$





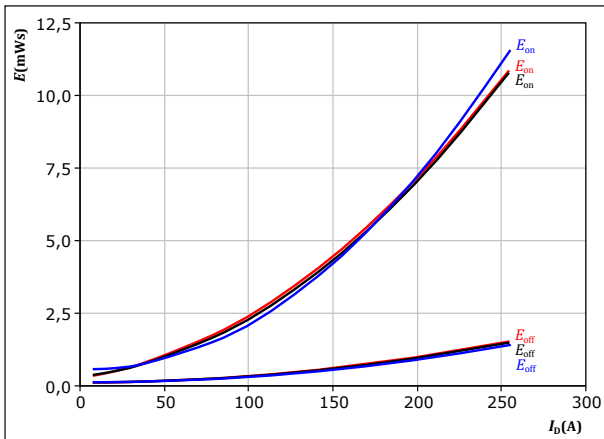
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Inverter Switching Characteristics

figure 8. MOSFET

Typical switching energy losses as a function of drain current

$$E = f(I_D)$$



With an inductive load at

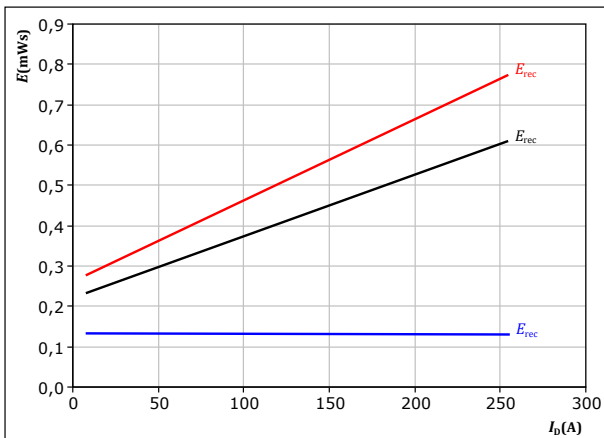
$V_{DS} = 600 \text{ V}$
 $V_{GS} = -5/15 \text{ V}$
 $R_{gon} = 2 \text{ } \Omega$
 $R_{goff} = 2 \text{ } \Omega$

T_j : 25 °C
125 °C
150 °C

figure 10. MOSFET

Typical reverse recovered energy loss as a function of drain current

$$E_{rec} = f(I_D)$$



With an inductive load at

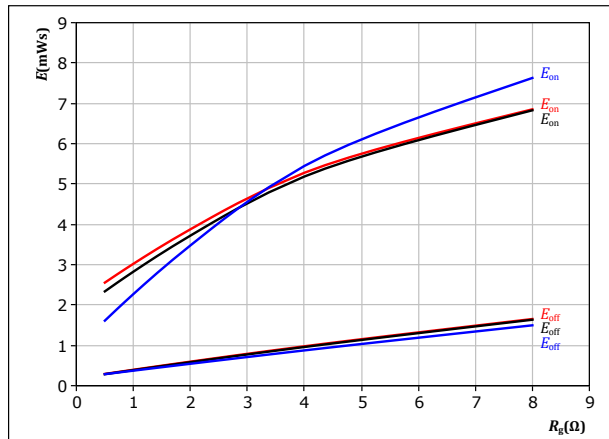
$V_{DS} = 600 \text{ V}$
 $V_{GS} = -5/15 \text{ V}$
 $R_{gon} = 2 \text{ } \Omega$

T_j : 25 °C
125 °C
150 °C

figure 9. MOSFET

Typical switching energy losses as a function of MOSFET turn on gate resistor

$$E = f(R_g)$$



With an inductive load at

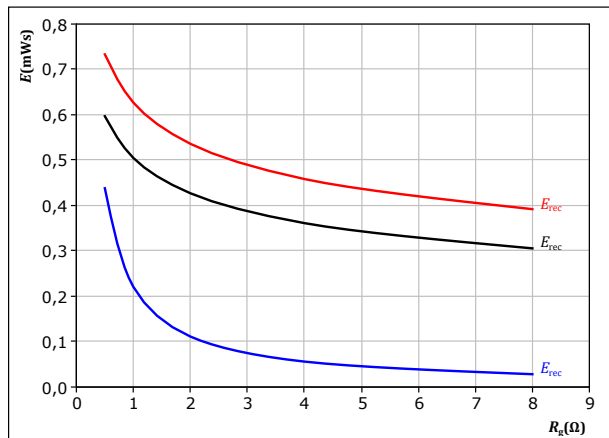
$V_{DS} = 600 \text{ V}$
 $V_{GS} = -5/15 \text{ V}$
 $I_D = 140 \text{ A}$

T_j : 25 °C
125 °C
150 °C

figure 11. MOSFET

Typical reverse recovered energy loss as a function of MOSFET turn on gate resistor

$$E_{rec} = f(R_g)$$



With an inductive load at

$V_{DS} = 600 \text{ V}$
 $V_{GS} = -5/15 \text{ V}$
 $I_D = 140 \text{ A}$

T_j : 25 °C
125 °C
150 °C

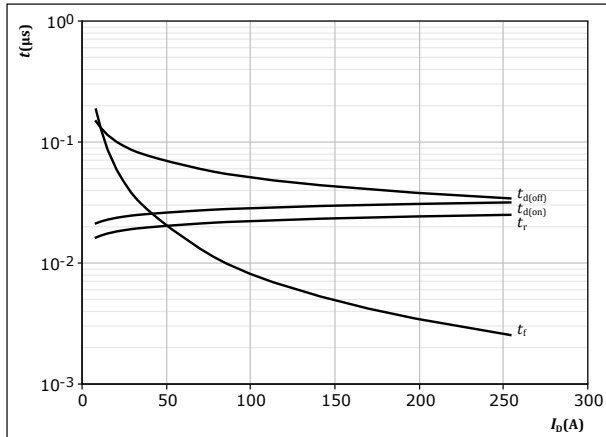


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Inverter Switching Characteristics

figure 12. MOSFET

Typical switching times as a function of drain current
 $t = f(I_D)$

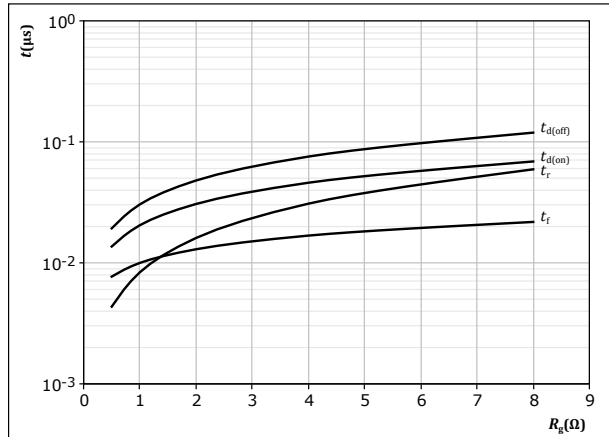


With an inductive load at

$T_j = 150$ °C
 $V_{DS} = 600$ V
 $V_{GS} = -5/15$ V
 $R_{gon} = 2$ Ω
 $R_{goff} = 2$ Ω

figure 13. MOSFET

Typical switching times as a function of MOSFET turn on gate resistor
 $t = f(R_g)$

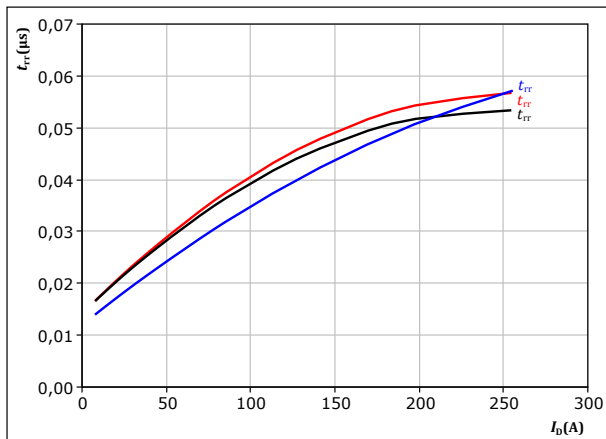


With an inductive load at

$T_j = 150$ °C
 $V_{DS} = 600$ V
 $V_{GS} = -5/15$ V
 $I_D = 140$ A

figure 14. MOSFET

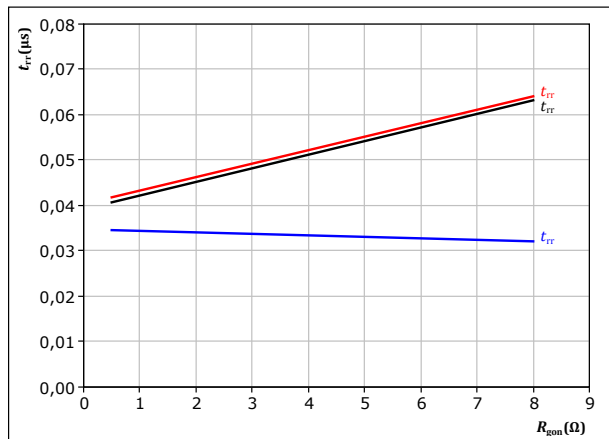
Typical reverse recovery time as a function of drain current
 $t_{rr} = f(I_D)$



At $V_{DS} = 600$ V
 $V_{GS} = -5/15$ V
 $R_{gon} = 2$ Ω
 T_j : 25 °C (blue), 125 °C (black), 150 °C (red)

figure 15. MOSFET

Typical reverse recovery time as a function of MOSFET turn on gate resistor
 $t_{rr} = f(R_{gon})$



At $V_{DS} = 600$ V
 $V_{GS} = -5/15$ V
 $I_D = 140$ A
 T_j : 25 °C (blue), 125 °C (black), 150 °C (red)



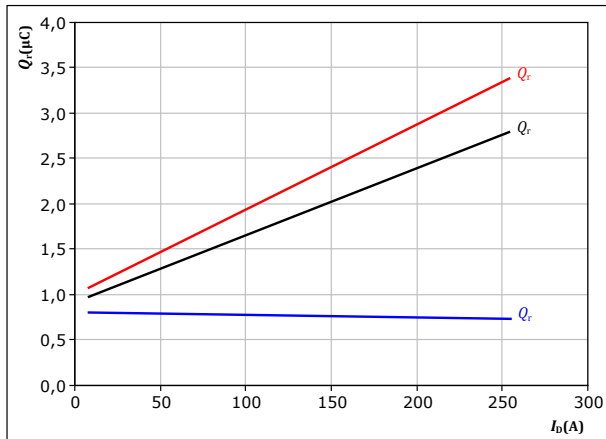
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Inverter Switching Characteristics

figure 16. MOSFET

Typical recovered charge as a function of drain current

$$Q_r = f(I_D)$$

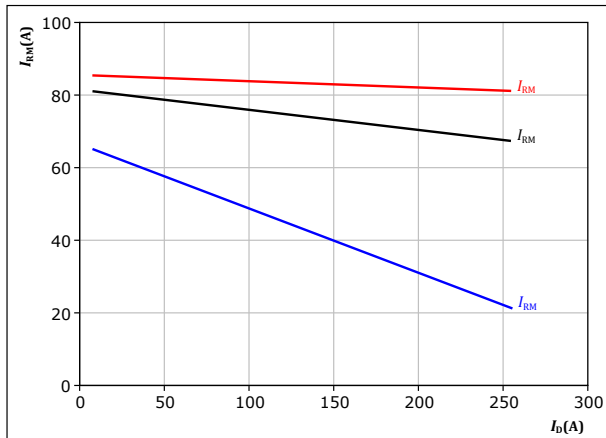


At $V_{DS} = 600$ V
 $V_{GS} = -5/15$ V
 $R_{gon} = 2$ Ω
 T_j : 25 °C (blue), 125 °C (black), 150 °C (red)

figure 18. MOSFET

Typical peak reverse recovery current as a function of drain current

$$I_{RM} = f(I_D)$$

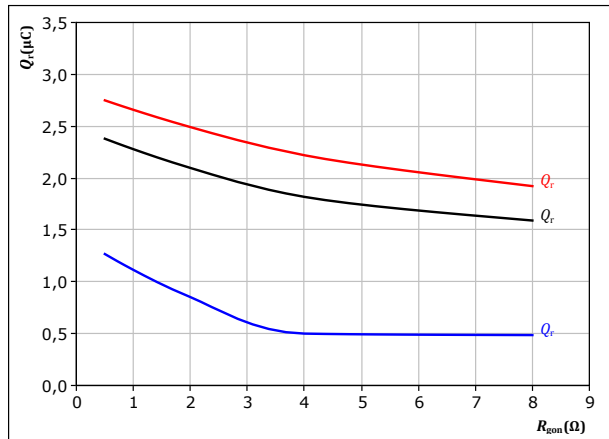


At $V_{DS} = 600$ V
 $V_{GS} = -5/15$ V
 $R_{gon} = 2$ Ω
 T_j : 25 °C (blue), 125 °C (black), 150 °C (red)

figure 17. MOSFET

Typical recovered charge as a function of MOSFET turn on gate resistor

$$Q_r = f(R_{gon})$$

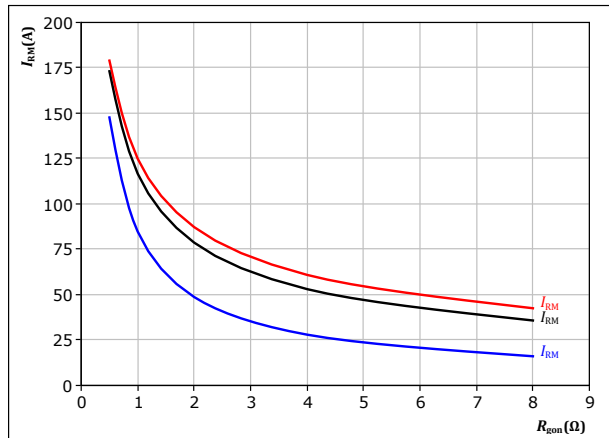


At $V_{DS} = 600$ V
 $V_{GS} = -5/15$ V
 $I_D = 140$ A
 T_j : 25 °C (blue), 125 °C (black), 150 °C (red)

figure 19. MOSFET

Typical peak reverse recovery current as a function of MOSFET turn on gate resistor

$$I_{RM} = f(R_{gon})$$



At $V_{DS} = 600$ V
 $V_{GS} = -5/15$ V
 $I_D = 140$ A
 T_j : 25 °C (blue), 125 °C (black), 150 °C (red)

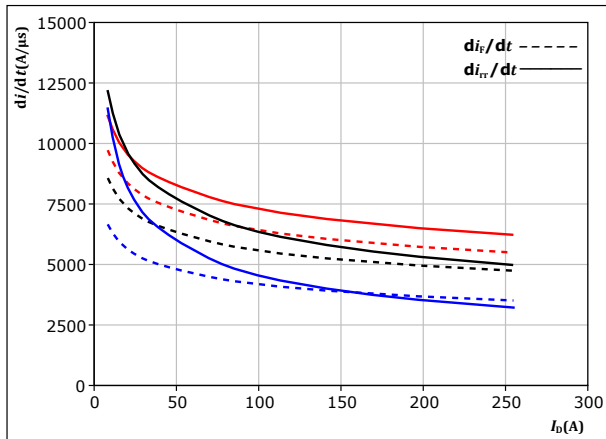


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Inverter Switching Characteristics

figure 20. MOSFET

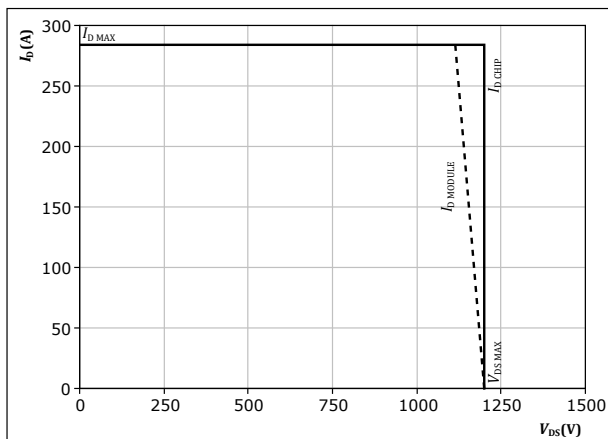
Typical rate of fall of forward and reverse recovery current as a function of drain current
 $di_f/dt, di_r/dt = f(I_D)$



At $V_{DS} = 600$ V
 $V_{GS} = -5/15$ V
 $R_{gon} = 2$ Ω
 $T_j = 25$ °C
125 °C
150 °C

figure 22. MOSFET

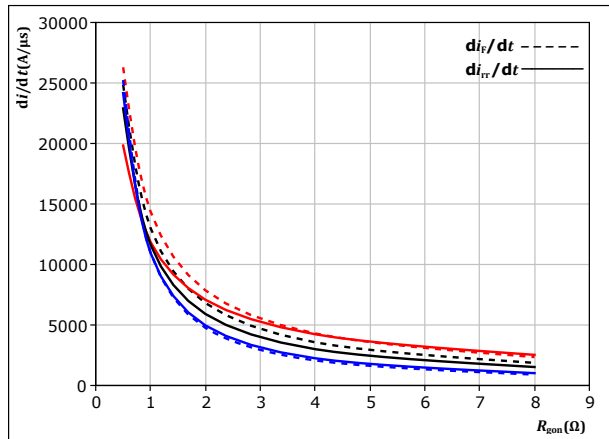
Reverse bias safe operating area
 $I_D = f(V_{DS})$



At $T_j = 150$ °C
 $R_{gon} = 2$ Ω
 $R_{goff} = 2$ Ω

figure 21. MOSFET

Typical rate of fall of forward and reverse recovery current as a function of turn on gate resistor
 $di_f/dt, di_r/dt = f(R_{gon})$



At $V_{DS} = 600$ V
 $V_{GS} = -5/15$ V
 $I_D = 140$ A
 $T_j = 25$ °C
125 °C
150 °C



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Inverter Switching Definitions

figure 23. MOSFET

Turn-off Switching Waveforms & definition of t_{doff} t_{Eoff} (t_{Eoff} = integrating time for E_{off})

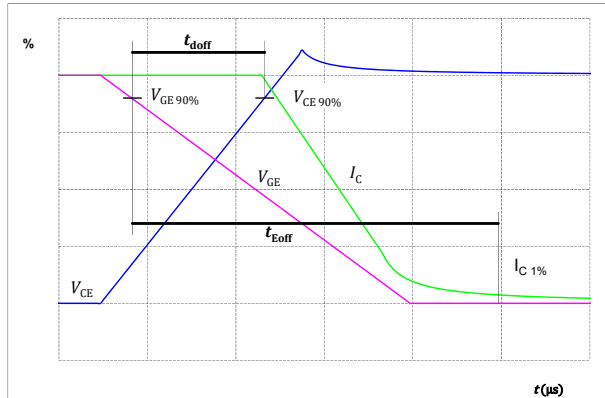


figure 24. MOSFET

Turn-on Switching Waveforms & definition of t_{don} t_{Eon} (t_{Eon} = integrating time for E_{on})

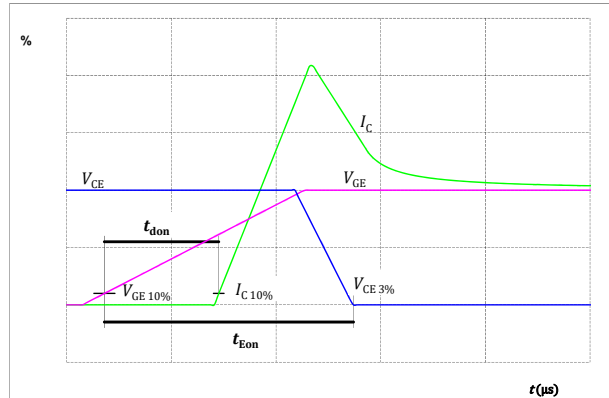


figure 25. MOSFET

Turn-off Switching Waveforms & definition of t_f

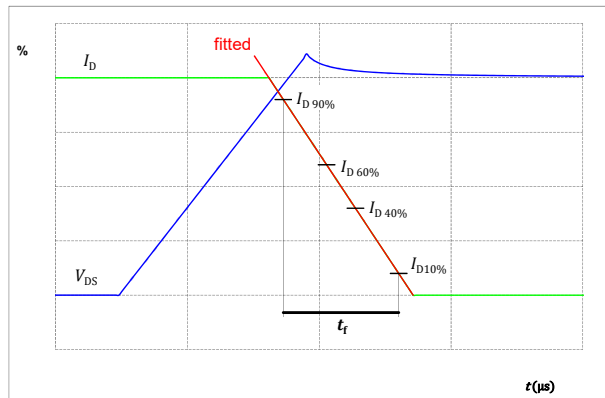
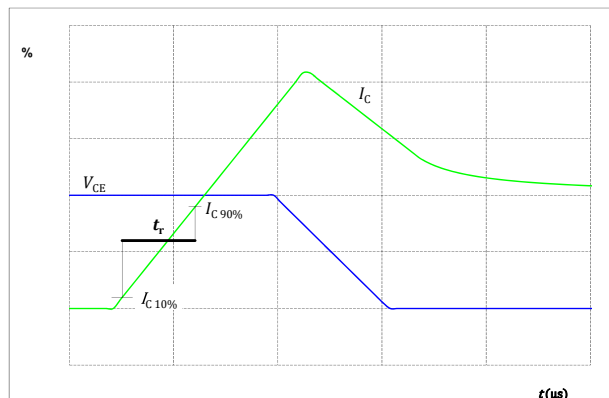


figure 26. MOSFET

Turn-on Switching Waveforms & definition of t_r





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Inverter Switching Definitions

figure 27. FWD

Turn-off Switching Waveforms & definition of t_{tr}

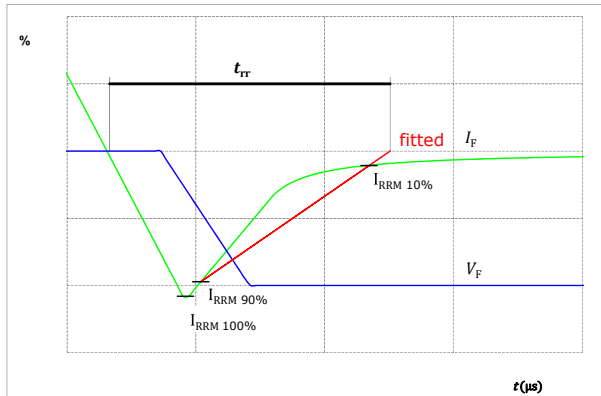


figure 28. FWD

Turn-on Switching Waveforms & definition of t_{Qr} (t_{Qr} = integrating time for Q_r)

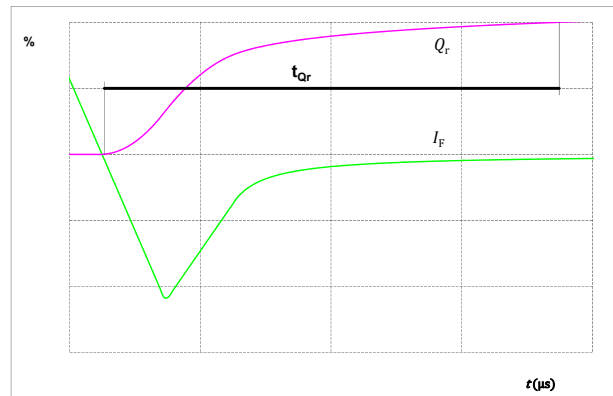
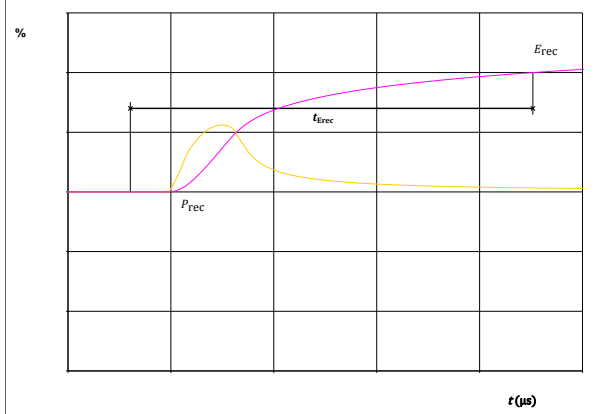


figure 29. FWD



Turn-on Switching Waveforms & definition of t_{Erec} (t_{Erec} = integrating time for E_{rec})





datasheet

Ordering Code	
Version	Ordering Code
Without thermal paste	10-EY126PB009MS-PJ19F78T
With thermal paste (5,2 W/mK, PTM6000HV)	10-EY126PB009MS-PJ19F78T-/7/

Marking							
	Text	Name		Date code	UL & VIN	Lot	Serial
		NN-NNNNNNNNNNNNN- TTTTTIVV		WWYY	UL VIN	LLLLL	SSSS
		Datamatrix	Type&Ver	Lot number	Serial	Date code	
TTTTTIVV			LLLLL	SSSS	WWYY		

Pin table [mm]			
Pin	X	Y	Function
1	6,4	0	G12
2	3,2	0	S12
3	0	0	Ph1
4	0	3,2	Ph1
5	0	6,4	Ph1
6	0	9,6	Ph1
7	6,4	19,2	G14
8	3,2	19,2	S14
9	0	19,2	Ph2
10	0	22,4	Ph2
11	0	25,6	Ph2
12	0	28,8	Ph2
13	3,2	38,4	Ph3
14	0	38,4	Ph3
15	0	41,6	Ph3
16	0	44,8	Ph3
17	0	48	S16
18	3,2	48	G16
19	12,8	48	Therm2
20	22,4	48	Therm1
21	25,6	48	G15
22	28,8	48	S15
23	32	48	DC-3
24	32	44,8	DC-3
25	32	41,6	DC-3
26	32	25,6	DC-2
27	32	22,4	DC-2
28	32	19,2	DC-2
29	32	16	S13
30	28,8	19,2	G13
31	22,4	12,8	DC+
32	19,2	12,8	DC+
33	16	12,8	DC+
34	19,2	16	DC+
35	22,4	16	DC+
36	25,6	32	DC+
37	22,4	32	DC+
38	22,4	35,2	DC+
39	25,6	35,2	DC+
40	32	6,4	DC-1
41	32	3,2	DC-1
42	32	0	DC-1
43	28,8	0	S11
44	25,6	0	G11

center of press-fit pin head

pin head type "T" with pushed-through-hole Ø4 mm, d259.7 - 0.006

for further PCB design rules refer to the latest handling instruction

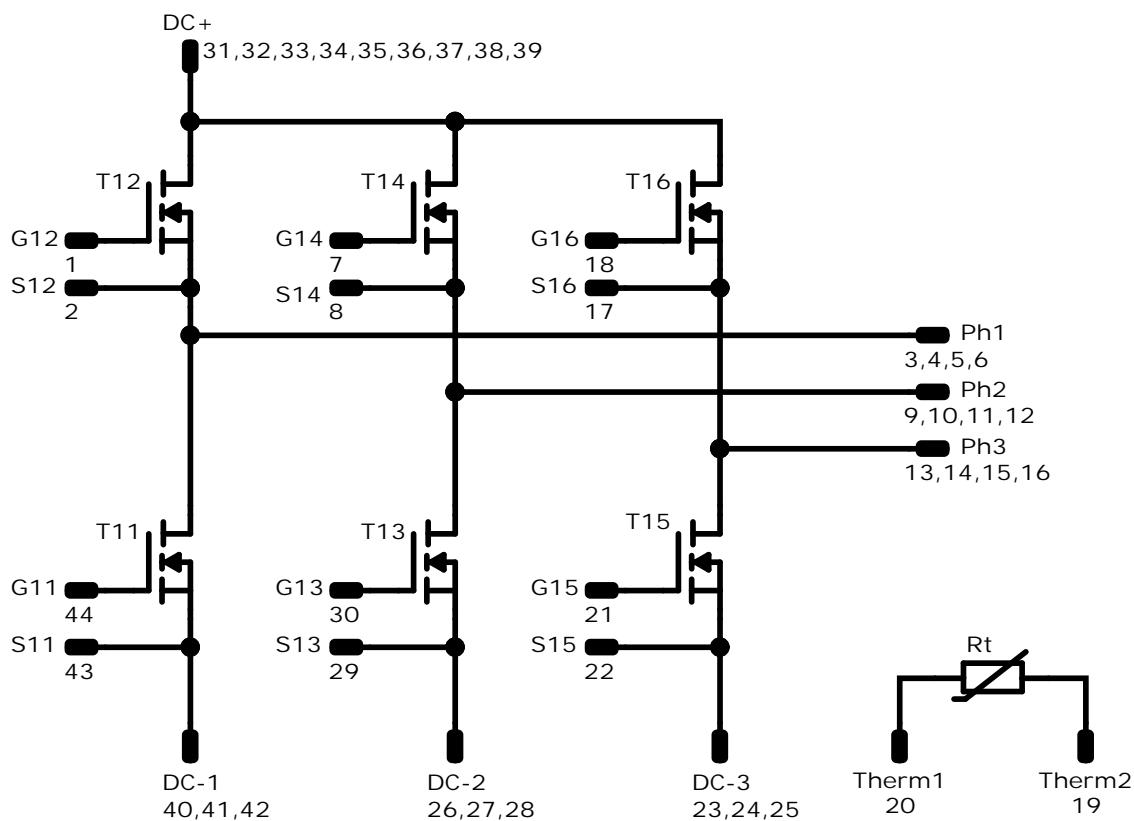
Tolerance of pinposition: ±0.05mm at the end of pins
Dimension of coordinate axis is only offset without tolerance



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Pinout




Identification

ID	Component	Voltage	Current	Function	Comment
T11, T12, T13, T14, T15, T16	MOSFET	1200 V	8,5 mΩ	Inverter Switch	
Rt	Thermistor			Thermistor	



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10-EY126PB009MS-PJ19F78T
datasheet

Packaging instruction				
Standard packaging quantity (SPQ) 100	>SPQ	Standard	<SPQ	Sample
Handling instruction				
Handling instructions for <i>flow</i> E2 packages see vincotech.com website.				
Package data				
Package data for <i>flow</i> E2 packages see vincotech.com website.				
Vincotech thermistor reference				
See Vincotech thermistor reference table at vincotech.com website.				
UL recognition and file number				
This device is UL 1557 recognized under E192116 up to a junction temperature under switching condition $T_{j,sp}=175^{\circ}\text{C}$ and up to 3500VAC/1min isolation voltage. For more information see vincotech.com website.				

Document No.:	Date:	Modification:	Pages
10-EY126PB009MS-PJ19F78T-D1-14	12 Nov. 2025	Initial Release	

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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.